

AMENDMENTS TO THE SPECIFICATION

Please replace the title on page 1, line 1, with the following:

-- IMAGE CAPTURING APPARATUS WITH MULTIPLE IMAGE CAPTURING DEVICES HAVING DIFFERENT CHARACTERISTICS --

Please replace the paragraph beginning on page 17, line 8, with the following rewritten paragraph:

-- The optical system 23 includes a photographic lens 31 disposed ahead of ~~a shutter~~ an aperture 35. Light passing through the aperture 35 impinges on a half-reflective mirror 32. Part of the light is reflected at 90 degrees from the surface of the half-reflective mirror 32 onto a total reflection mirror 33. The total reflection mirror 33 redirects the light onto the surface of the second image capturing device D2. The remaining part of the light impinging on the part reflective mirror 32 passes therethrough, and then through ~~an aperture~~ a mechanical shutter 36 on its path to the first image capturing device D1. Generally the part reflective mirror 32 splits the light into roughly equal parts, with one part reflected, and the other part transmitted. The photographic lens 31 includes conventional zoom and focusing mechanisms. Since such mechanisms are conventional, further description and illustration thereof is omitted. The aperture 35 is located on the side of the half reflective mirror 32 nearer the subject, i.e., on the side nearer the photographic lens 31. The mechanical shutter 36 is located on the side of the half reflective mirror 32 nearer toward the first image capturing device D1. --

Please replace the paragraph beginning on page 18, line 8, with the following rewritten paragraph:

-- The processing means 26 includes an analog processing circuit 41 receiving data from the first and the second image capturing devices D1 and D2. An image capturing device driving circuit 42 controls the image capturing devices D1 and D2 and the analog processing circuit 41. The processing means 26 also contains an analog-to-digital converter 44 which digitizes the image ~~date~~ data from the analog processing circuit 41 and applies the digitized result to an image processing circuit 45. The image processing circuit 45 is connected to a bus, to which the

recording means 25. The bus is also connected to a display control circuit 47 which feeds display signals to the display means 24. --